

Amendments to the Specification

Please change the Title to the following:

APPARATUS AND METHOD FOR SPECULATIVELY PERFORMING A RETURN
INSTRUCTION IN A MICROPROCESSOR

Please replace the table on page 1 with the following amended table:

| Docket # | Serial # | Title |
|-----------|------------------|--|
| CNTR:2021 | <u>09/849736</u> | SPECULATIVE BRANCH TARGET ADDRESS CACHE |
| CNTR:2022 | <u>09/849658</u> | APPARATUS, SYSTEM AND METHOD FOR DETECTING AND CORRECTING ERRONEOUS SPECULATIVE BRANCH TARGET ADDRESS CACHE BRANCHES |
| CNTR:2023 | <u>09/849734</u> | SPECULATIVE HYBRID BRANCH DIRECTION PREDICTOR |
| CNTR:2052 | <u>09/849799</u> | SPECULATIVE BRANCH TARGET ADDRESS CACHE WITH SELECTIVE OVERRIDE BY SECONDARY PREDICTOR BASED ON BRANCH INSTRUCTION TYPE |
| CNTR:2062 | <u>09/849754</u> | APPARATUS AND METHOD FOR SELECTING ONE OF MULTIPLE TARGET ADDRESSES STORED IN A SPECULATIVE BRANCH TARGET ADDRESS CACHE PER INSTRUCTION CACHE LINE |
| CNTR:2063 | <u>09/849800</u> | APPARATUS AND METHOD FOR TARGET ADDRESS REPLACEMENT IN SPECULATIVE BRANCH TARGET ADDRESS CACHE |